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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10053572	FILING DATE 01/24/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER
**APPLICANTS: Ohnuma Hideto.					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-019293 01/26/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		740758-2422	
Verified and Acknowledged Examiner's Initials					
TITLE : Method of manufacturing semiconductor device					

U.S. DEPT. OF COMM. PAT. & TM. PTO-438U/Rev. 12-96

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drawg.	Flgs. Drawg. Print Flg.
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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